

DPW

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the Application of:

Koji HOSONO et al.

Serial No.: 10/664,977

Filed: September 22, 2003

For: NON-VOLATILE SEMICONDUCTOR
MEMORY

Atty. Docket No.: 002372.00046

Group Art Unit: 2824

Examiner: Nguyen, V.

Confirmation No.: 4948

RESPONSE TO RESTRICTION REQUIREMENT

U.S. Patent and Trademark Office
220 20th Street S.
Customer Window, Mail Stop Amendment
Crystal Plaza Two, Lobby, Room 1B03
Arlington, VA 22202

Sir:

This paper is responsive to the office action mailed April 4, 2005 in connection with the above-identified patent application and is filed prior to the expiration of the one (1) month period for response set therein.


In response, Applicants elect, without traverse, the invention of Group I, i.e., claims 1-11, and 13, drawn to EEPROM with volatile signal storage device. Applicants reserve the right to file a divisional application directed to the subject matter of the non-elected claims prior to the termination of proceedings in this patent application.

No fee is believed to be associated with the filing of this paper. Nonetheless, should the U.S. Patent and Trademark Office determine that a fee is required, authorization is given to charge our Deposit Account No. 19-0733.

Respectfully submitted,
BANNER & WITCOFF, LTD.

Dated: April 27, 2005

By:



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